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Sheet 1 of 1



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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

APPLICANTS: Pierre-Yves BURGI, et al.GROUP ART UNIT: 2631SERIAL NO.: 10/532,090ATTY. DOCKET NO.: P70540US0FILING DATE: April 21, 2005TODAY'S DATE: December 29, 2005

U.S. PATENT DOCUMENTS

| *EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB- CLASS | FILING DATE (If Appropriate) |
|----------------------|----|--------------------|----------|--------|-------|---------------|---------------------------------|
| | AA | 6,567,485 B1 | 05/20/03 | Venier | | | |
| | AB | | | | | | |
| | AC | | | | | | |
| | AD | | | | | | |
| | AE | | | | | | |
| | AF | | | | | | |
| | AG | | | | | | |

FOREIGN PATENT DOCUMENTS

| *EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUB- CLASS | Translation | |
|----------------------|----|--------------------|----------|---------|-------|---------------|-------------|------|
| | | | | | | | (Yes) | (No) |
| | AH | EP 0 961 207 A1 | 12/01/99 | EP | | | Equivalent | |
| | AI | EP 1 150 250 A1 | 10/31/01 | EP | | | Abstract | |
| | AJ | | | | | | | |
| | AK | | | | | | | |
| | AL | | | | | | | |

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

| | | |
|--|----|--|
| | AM | Mortara, A., et al., "A Communication Scheme for Analog VLSI Perceptive Systems", IEEE Journal of Solid-State Circuits, No. 6, June 6, 1995, pps. 660-669. |
| | AN | |
| | AO | |
| | AP | |

| | | | |
|----------|----------------|-----------------|------------|
| EXAMINER | /Wenpeng Chen/ | DATE CONSIDERED | 04/23/2009 |
|----------|----------------|-----------------|------------|

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /WPC/